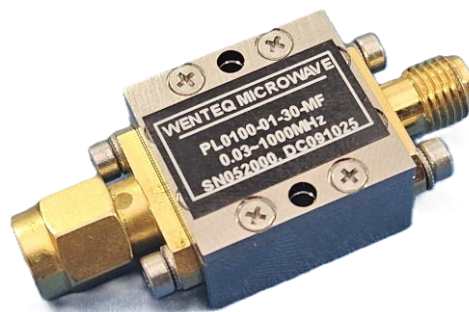


Features:

- Operation frequency from 30kHz to 1GHz
- Power Handling: 1W CW
- Low insertion loss, good VSWR
- DC block, 50 ohm input/output impedance
- SMA F-F, F-M, or M-M RF connectors options
- Small size with two mounting holes
- Operating temperature -40~+85°C, storage temperature -55~+125°C

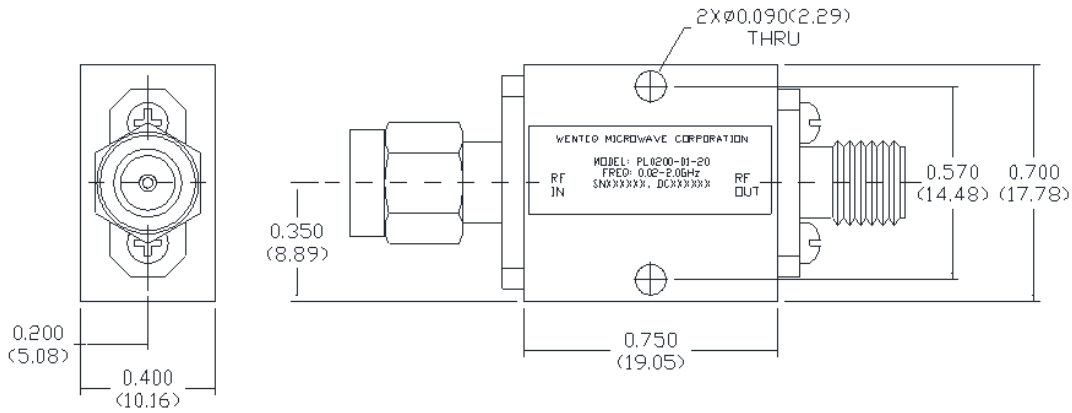
**Description:**

The PL0100-01-30 is a two stage PIN diode power limiter designed to protect other sensitive electronic devices such as low noise amplifiers against ESD and input RF power surges up to +30dBm power over the frequency range from 30kHz to 1GHz. The limiter insertion loss starts to roll off at about +7dBm and with a leakage power of about +10dBm at +30dBm input power.

Electrical Specification

Parameters	Units	Specifications		
		Minimum	Typical	Maximum
Frequency Range	MHz	0.03		1000
Small Signal Insertion Loss	dB		0.20	0.5
Small Signal VSWR	-			1.2:1
Compression Starting Power	dBm		+7.0	
Leakage power at +30dBm input	dBm		+10.0	+13.0
Power handling without damage	dBm	+30.0		
Operating Temperature	°C	-40		+85
Survival Temperature	°C	-55		+125
RF connectors Default PL0100-01-30-MF Optional: PL0100-01-30-FM Optional: PL0100-01-30-FF And PL0100-01-30-MM			SMA male/female SMA female/male SMA female/female SMA male/male	
Size	inches	0.75"x0.70"x0.40"		

Mechanical Structure:

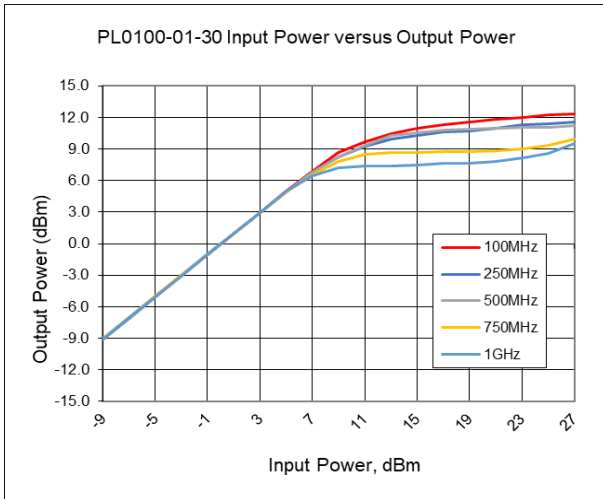
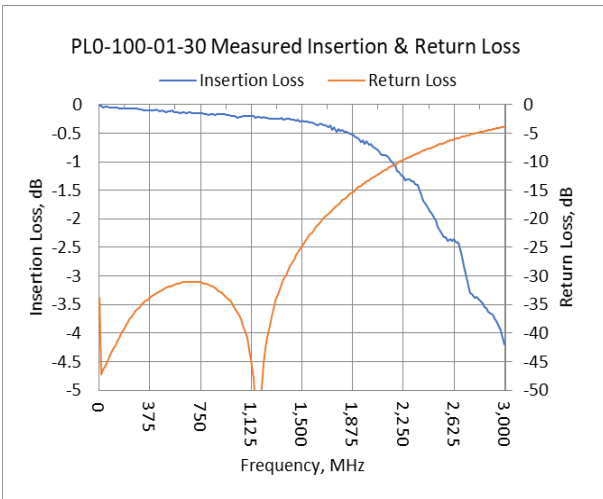


Note: All units in inches (mm).

Housing Material and Surface Finish:

- Body and cover material: aluminum
- Surface finish: nickel plated
- Connector material: Copper
- Connector surface finish: gold plated

Typical Test Plots



Revision History:

Revision	Date	Description	Comments
A00	09/10/2025	Initial Release	